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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 1

Complete if known

Application Number	10/665,757
Filing Date	September 19, 2003
First Named Inventor	Glenn J. Leedy
Art Unit	2822
Examiner Name	Pamela E. Perkins
Attorney Docket Number	ELM-1 CONT. 9

U.S. PATENT DOCUMENTS

Examiner initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		4,954,875	09-04-90	Clements	

FOREIGN PATENT DOCUMENTS

Examiner initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ¹ - Number ² - Kind Code ³				
		EP 0 703 618 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.		
		EP 0 703 619 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.		

NON PATENT LITERATURE DOCUMENTS

Examiner initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
		Mitsumasa Koyanagi et al., "Design of 4-KBIT X 4-layer Optically Coupled Three-Dimensional Common Memory For Parallel Processor System," IEEE Journal of Solid-State Circuits, Vol. 25. No. 1, February 1, 1990.	

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